Correlator Implementation and Control meeting, November 15 2012, 11.00 local time
Arpad's room

present Jonathan, Salvatore, Harro, Des, Arpad

action items
Jonathan: test fix of Erlang performance problems reading UDP packets (on hold)
Jonathan + Salvatore: figure out how to make packet spacing adjustable (on hold)
Harro: create more N11L4 VDIF data
#not done remains
Harro: talk to Ger about other plotting packages besides Glish
#not done remains
Harro: get sshfs working on Dop233 so that developers can use it without transplanting complete environment
#done remove
Harro: send ticket to Paul about upgrade of Ubuntu on unictl
Done remove. Discussed with Paul, put in a ticket. The urgency is low however, could simply update Erlang, as a local package.
Harro: ask Paul if AriBox is reachable
#done remove

new actions

Harro: update Erlang on unictl
Des: investigate Suse and Erlang, how to keep Erlang synched across platforms
Harro: ask Paul about 10G on AriBox
all: decide on topic and scope of documents to provide for design review

updates

Jonathan: lots of simulations ongoing, bugfixing. Test benched corner turning, fixed bug. Packet loss is still puzzling. There seems to be an addressing bug, after fix Harro should try tcpdump. BN1: all packets lost? BN3: part of packets lost.

Salvatore is setting up a matlab environment for tests, bug fixes need to be done first. Mixer-FB-FFT-corner turner.

Des to start testing. Next week should decide on what documents to provide for review, date is put on the 11th of December, docs should be at

dr. Bob and Sergei one week before.

Since then, it was found that dr. Bob will only be back from the States on the 10th, without opportunity to look at any documentation. Will have to be moved to January 2013. Most of docs still should be finished before end of year.

a certain amount of pressure is felt downstairs, getting data in
but
mangled data out. Output FN seems fine, signal tap in BN between corner turning and validity accumulator. Will make test bench with only corner turner simulation to save time, whole BV takes 5 hours. Maybe fault in cornerturning. Used to do single integration, now continuous, used to do 2 stations, now 8. Maybe memory mapping wrong.
Discussion about long times for simulation. Dop233 currently not used, because it is not easy to copy complete development environment. Not needed, should be possible to mount home disks from dop233, using sshfs (NSF no good, different domains). ***Action Harro***
New PCs not really a solution, as they have more cores but most tools are single threaded anyway.
Des: could use design anyway to start testing getting data into UB, even if output is rubbish. Harro: need more VDIF data? Answer yes.
Salvatore: is it possible to run everything on Unictl. Harro: yes, possible, use a node on Unictl to talk to FPGA. Has old Ubuntu, old Erlang. **action Harro** send ticket to Paul.
Jonathan: 10G from UB to what? AriBox most obvious, ***action Harro** ask Paul if AriBox reachable.