JUC, 15/1/2016, Arpad's room

present Jonathan, Harro, Des, Arpad

Harro: last week Jonathan told him stitching of bands to make 32 MHz was wrong. And he was right. Has been fixed. Des: also bug in JSON, also fixed.

Jonathan: stitching indeed seems fixed. However, big differences in amplitude scale when comparing 16 and 32 MHz firmware. Does scale with integration time, delay does not seem to influence it. Might be missing data, although weights seem ok. Will put in signal taps.
Des: might be useful to use sine waves again. Maybe things like scaling up of delay coefficients wrong? Investigate, investigate

Des: 16MHz stuff is not broken by changes to control code to include 32MHz. May need a little more testing, but seems quite ok.
Erlang script has been written to launch code from ccsbeta, and python script to launch erlangs etc. Both need to be launched from eBob script. Should be ready by next week (?) Then, hand over to operators and see what breaks.

Last point in to-do list, app-reset, needs to be looked at. Problem has not recurred lately.

Some discussion on e. Cornerturning has to be done somewhere, fila10G right place. Test e simulated in-house with mk5s. Then use part of e-time with few fila10G stations. Need to talk to Gino about packet size of cornerturned VDIF.