

# Time Transfer in a Wide Area White Rabbit Network

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12. November 2014

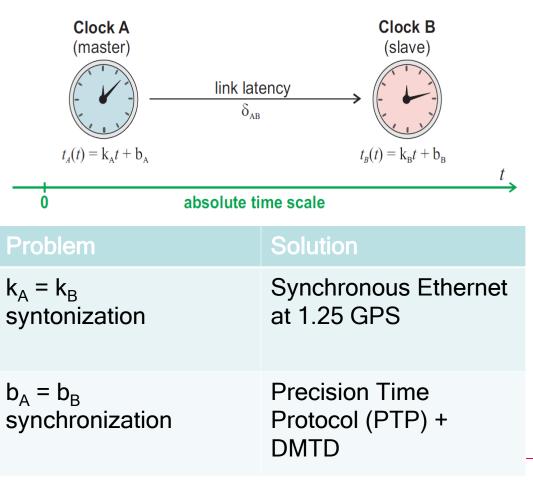
## White Rabbit

- Timing & Control of the LHC @ CERN: Deterministic data transfer and accurate clock synchronization
- Open Hardware & Software
- Up to 10 km link length
- < 1ns time offset between more than 1000 nodes</li>



"I'm late, I'm late, for a very important date! No time to say hello, goodbye..."

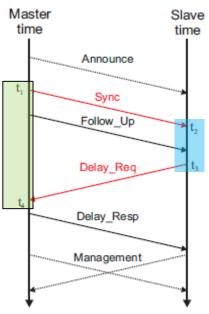
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## White Rabbit Algorithm

(Step 0: SyncE provides <u>syntonized</u> clocks)

Step 1. Hardware time-stamping of  $t_1-t_4$ Gives coarse (8 ns) RTT

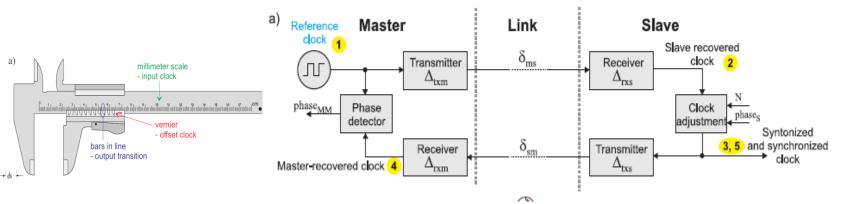


Ston 2. Enhance t t

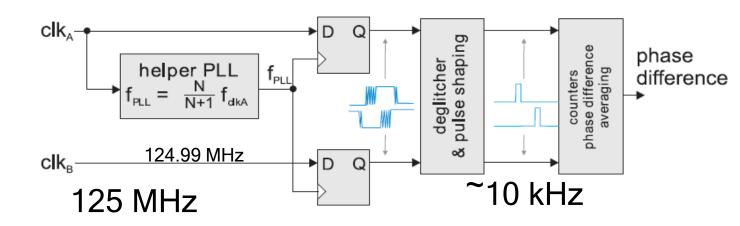
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 $\underline{delay\_coarse} = (t_4 - t_1) - (t_3 - t_2)$ 

### Phase measurement using DMTD = Dual Mixer Time Difference



**FPGA Implementation in WR-node** 

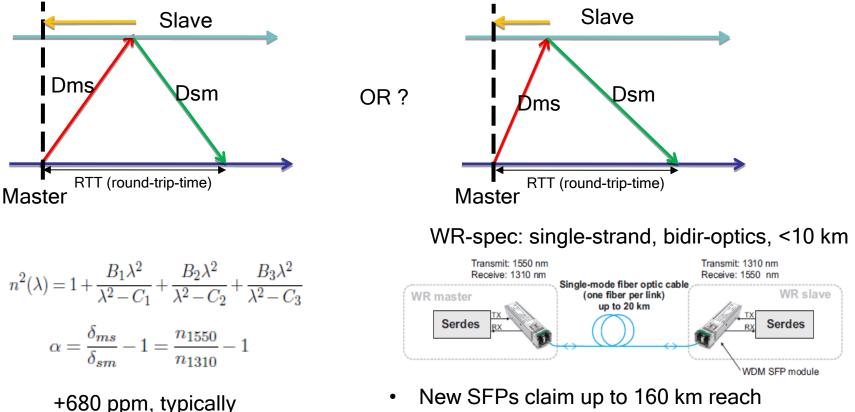


**ZMIKES** 

[T.Włostowski]

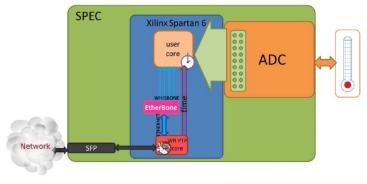
#### Fiber asymetry

### not known beforehand, must be calibrated



- New SFPs claim up to 160 km reach (with new bidir-amplifiers even further)
- White Rabbit works with duplex (two-strand) SFPs also

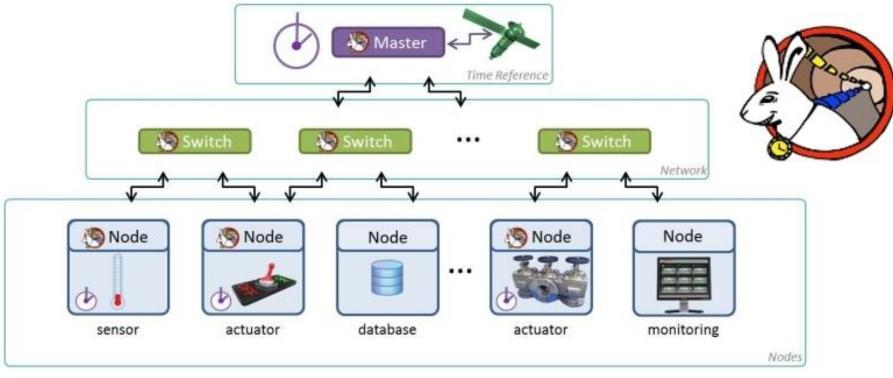
### Many point-to-point links: WR Network



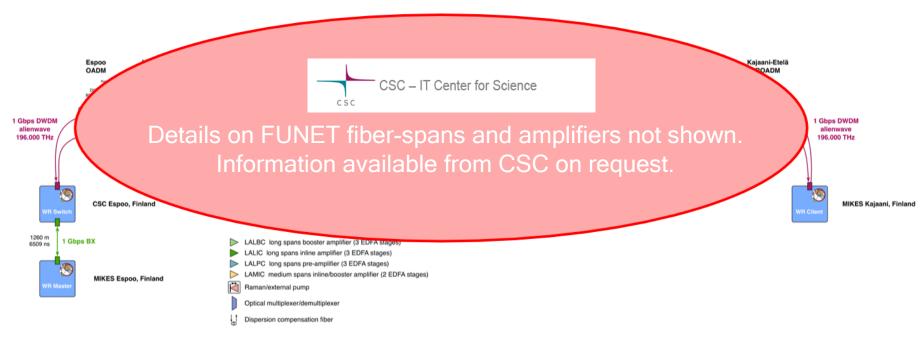
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WR Node:

- •SFP + FPGA + FMC front-end
- Open hardware
- Open firmware/software



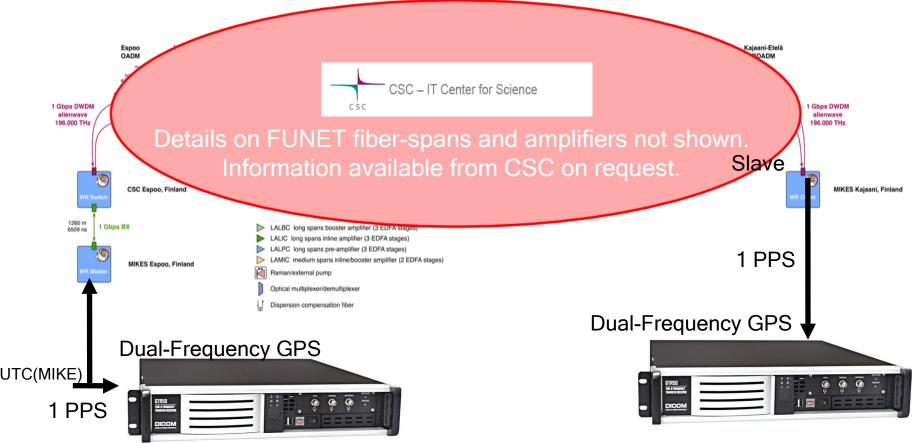
## Espoo-Kajaani time-transfer experiment in FUNET



- 1000 km Espoo-Kajaani light-path in FUNET
  - 10% of fiber is in DCF spools
- Duplex (two-strand) SFPs on ITU-T #60 (196 THz)
- GrandMaster node in Espoo locked to UTC(MIKE)

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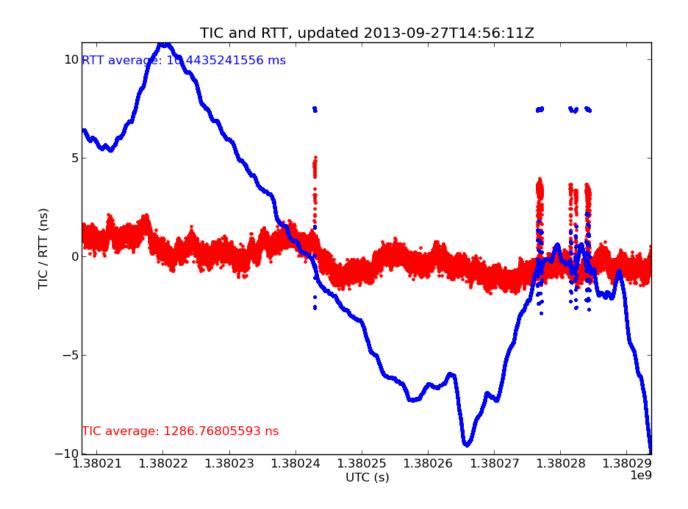
#### Independent verification by GPS-PPP



- 1. Receivers colocated, common-clock calibration (5 days)
- 2. Receivers at Espoo/Kajaani, Fiber-asymmetry calibration (1 day)
- 3. Data collection (100 days)

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### Initial problems with 8 ns RTT jumps

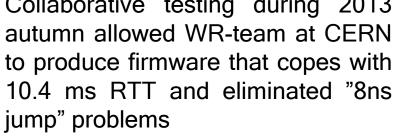


#### Link operation started 2013 September

	•		WhiteRabbit Espoo-Kajaani link RTT variations MIKES & CSC, Finland, 2013 Aug 8-9
WR PTP Core Sync Monitor Esc = exit	v 1.0	60	
TAI Time:	Fri, Aug 16, 2013, 13:55:37		
wru1: Link up (RX: 4290	, TX: 1916), mode: WR Slave Locked Calibrated	40	$\mu_{ m RTT} = 10.431119812~ m ms$ –
Synchronization status:			
Servo state: Phase tracking: Synchronization source: Aux clock status:	TRACK_PHASE ON wru1 0:disabled	20 - (11)	
Master-slave delay: Master PHY delays: Slave PHY delays: Total link asymmetry:	10445954558 ps 5223650621 ps TX: 0 ps, RX: 175200 ps TX: 46407 ps, RX: 169443 ps -1346684 ps 10445563508 ps 0 ps 235 ps -4 ps 0 ps 943	-20 - -40 -	
Collaborative testing during 2013 autumn allowed WR-team at CERN		-60 L	RTT varies strongly with temperature:

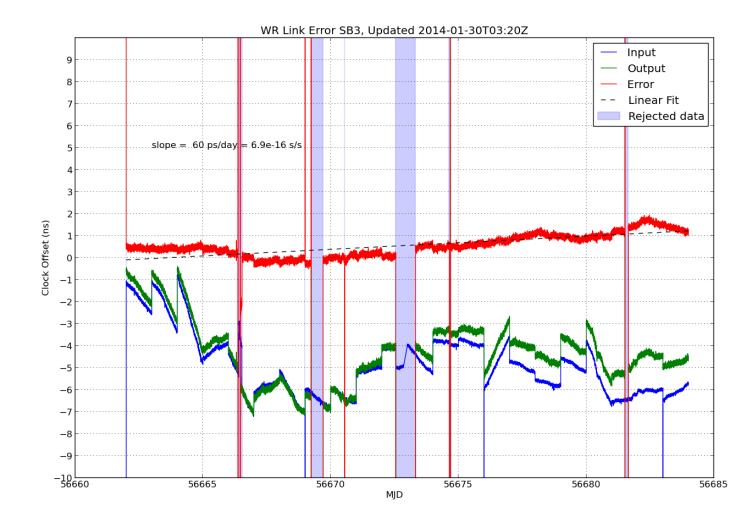
- -Seasonal +/- 200 ns or more
  - -Daily +/- 50 ns in spring/autumn

-WR asymmetry parameter is ca. -800 ppm (uplink is 4 us shorter than downlink)

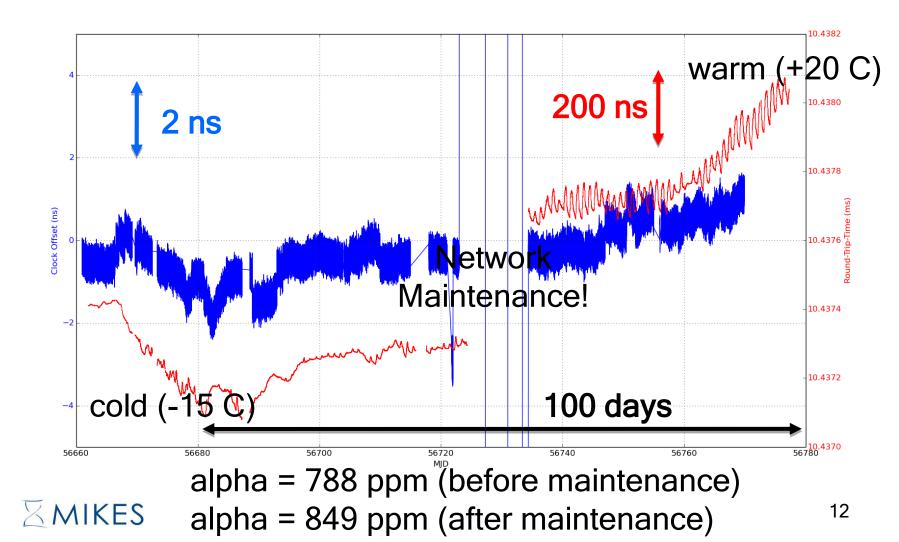


### Link Performance

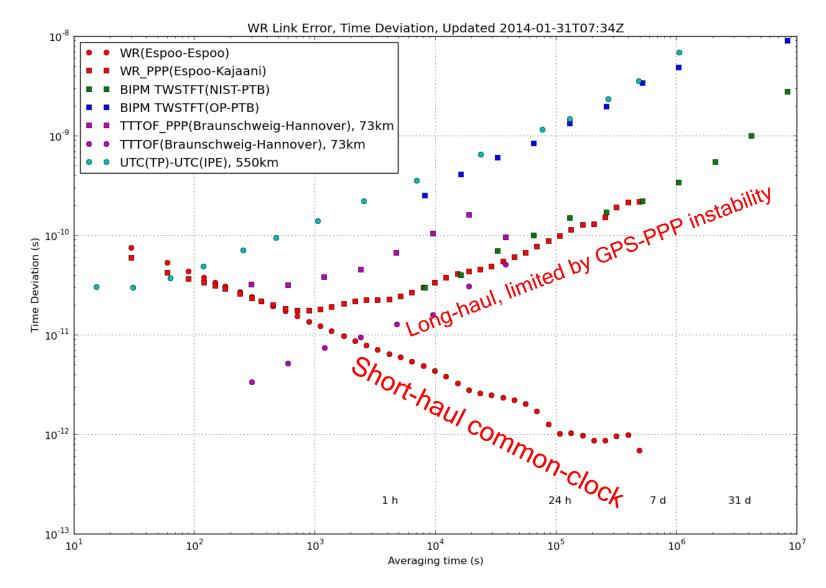
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#### Link Error vs. Round-Trip-Time



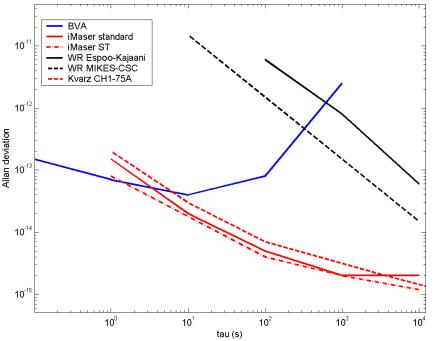
### **Time Deviation**



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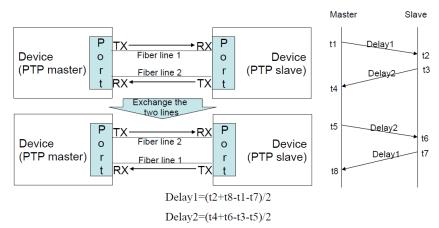
#### **Outlook / Challenges**

1. Short-term stability



- Improve PLL between external clock and GM-node
- PLL tuning between Master and Slave
- Better local-oscillator (OCXO) on SPEC (?)

3. Additional Phase-servo inputs e.g. Temperature (Fiber, DCFs, Nodes)



Reverse asymmetry by: •Exchanging Tx/Rx fibers (twostrand) •Exchanging Tx/Rx wavelengths (single-strand) [Huang, China Mobile]

4. Fiber-Fiber comparisons Not limited by satellite technique

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## Acknowledgments:

- CSC for the long-haul fiber
- CERN BE-CO group for developing&supporting WR (T.Wlostowski, G.Daniluk etc.)
- Seven Solutions Hardware provider & support
- The NEAT-FT EMRP program

EMRP



European Metrology Research Programme Programme of EURAMET

The EMRP is jointly funded by the EMRP participating countries within EURAMET and the European Union

Thank You!