

JUC meeting, September 12 2013, Arpad's room

present Salvatore, Jonathan, Harro, Des, Arpad

Salvatore: read side of packetRX investigated. 1st version without delay + validity, check if >5000 packets are possible. Write side done, read side: new design, some re-use of old modules. Initial version: 1 month, add book keeping, another month.

Jonathan: has been helping Salvatore understanding the existing code. Re-thinking makes more understandable code, which is good. Reading the docs for DSP builder. Lends itself for time-multiplexed operations, FIR also PF. Try to replicate current design. Matlab license still problem, need to buy fixed-point toolbox. Waiting for Jan to return from vacation.

Des: clock offset works, some weirdness. Des machine encounters enormous packet loss. Too early to tell about missing fringes, maybe wrong routing via Astron network?

JUC meeting, September 25 2013, Arpad's room

present Salvatore, Jonathan, Harro, Des, Arpad

Jonathan: after talking to Aard, insert  $\pi/2$  phase correction, fractional delay at center of band. Fringes, but still noisy, but may be due to fractional correction that was taken out and not yet replaced.

Salvatore: changes to packetRX architecture, started coding this week, main control unit first, extend with smaller controllers