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TO: Mark 5B design group

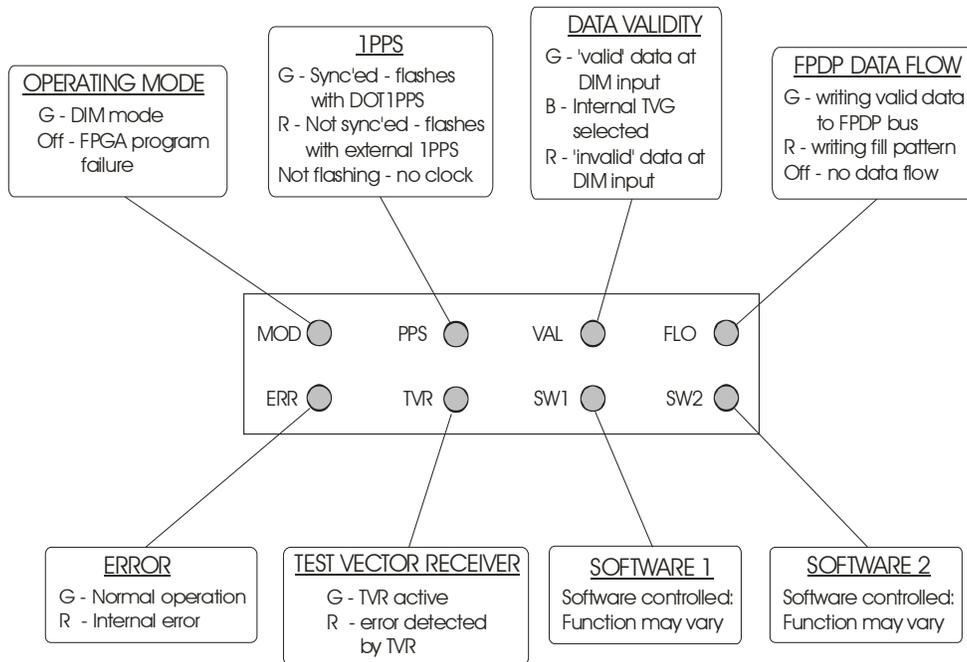
FROM: R. Cappallo, A. Whitney

SUBJECT: Mark 5B status LED's – functions and layout

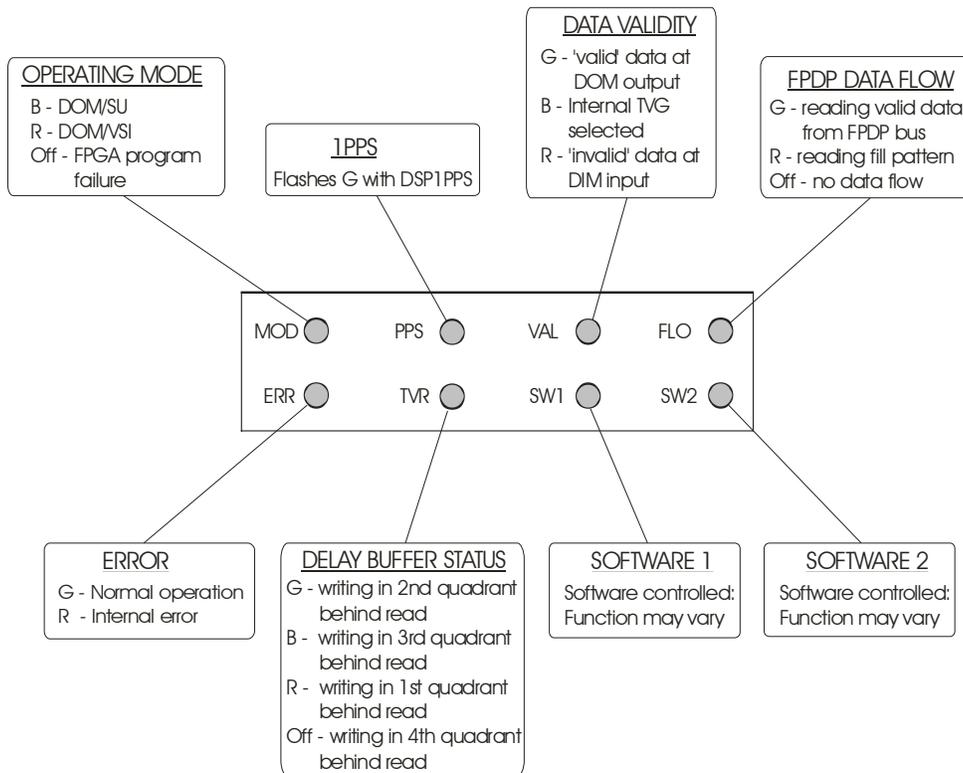
8 tri-colored LED's are suggested, with the following functions:

Label	Meaning	DIM mode	DOM mode
MOD	Operating mode	G – DIM B – DOM/SU R – DOM/VSI Off – FPGA programming failure	
PPS	1PPS	Flash at 1PPS rate: R - <u>Not sync'ed</u> ; flashes with selected external 1PPS G - <u>Sync'ed</u> ; flash with DOT1PPS Not flashing – no clock	Flash G with DSP1PPS
VAL	Data validity	G – 'valid' data at DIM input B – TVG selected and active R – 'invalid' data at DIM input	G – 'valid' data at DOM output B – TVR active; TVG data recognized R – 'invalid' data at DOM output
FLO	FPDP data flow	G – writing valid data to FPDP R – writing fill pattern Off – no data flow	G – reading valid data from FPDP R – reading fill pattern Off – no data flow
ERR	Error	G – normal operation R - Internal error detected	
TVR	Delay buffer state	G – TVR active R – TVR error detected	G – write in 2 nd quadrant behind read B – write in 3 rd quadrant behind read R – write in 1 st quadrant behind read Off – write in 4 th quadrant behind read
SW1	Software controlled	-	-
SW2	Software controlled	-	-

Key: G – green R – red B – blue



Mark 5B DIM Status LEDs



Mark 5B DOM Status LEDs